

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Albert S. Weiner

Title: Non-volatile transistor memory array incorporating read-only elements with single mask set

Docket No.: 2800.450US1  
Filed: March 26, 2004  
Examiner: Tinh Nguyen  
Customer No.: 76287

Serial No.: 10/810,035  
Due Date: July 21, 2009  
Group Art Unit: 2818  
Confirmation No.: 8458

Notice of Allowance Date: April 21, 2009

**Mail Stop Issue Fee**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

We are transmitting herewith the following:

- ☒ Authorization to charge Deposit 19-0743 in the amount of \$1510.00 to cover the Large Entity Issue Fee Payment.
- ☒ Authorization to charge Deposit 19-0743 in the amount of \$300.00 to cover the Publication Fee Payment.
- ☒ Issue Fee Transmittal (Form PTOL-85).
- ☒ Communication Re: Fee Address (1 page).

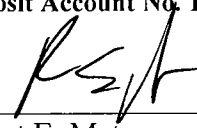
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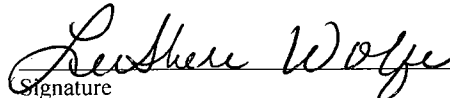
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being filed using the USPTO's electronic filing system EFS-Web, and is addressed to: Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 21<sup>st</sup> day of July, 2009.

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